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APPLICANTS

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** CONTINUING DATA *****
 This application is a 371 of PCT/JP04/00788 01/28/2004 *BV*

** FOREIGN APPLICATIONS *****
 JAPAN 2003-030511 02/07/2003 *BV*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowances	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

Verified and
Acknowledged

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TITLE
 Apparatus and method for inspecting thin film transistor active matrix substrate

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